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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
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CONFIRMATION NO. 1126

23125 FREESCALE SEMICONDUCTOR, INC. LAW DEPARTMENT 7700 WEST PARMER LANE MD:TX32/PL02 AUSTIN, TX78729

FILING RECEIPT

Date Mailed: 04/19/2007

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Applicant(s)

Hassan Ihs, Toulouse, FRANCE;

Assignment For Published Patent Application

FREESCALE SEMICONDUCTOR, INC., Austin, TX

Power of Attorney: The patent practitioners associated with Customer Number 23125

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP04/14912 11/26/2004

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 03292967.1 11/28/2003

If Required, Foreign Filing License Granted: 04/12/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/596,043

Projected Publication Date: 07/19/2007

Non-Publication Request: No

Early Publication Request: No

Title

Clock pulse generator apparatus with reduced jitter clock phase

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